

FEATURES:

- 8-PIN PACKAGE
- 3 INDEPENDENT DELAYS
- TTL SCHOTTKY INTERFACED
- DELAYS TIMES FROM 5-500nS

ELECTRICAL CHARACTERISTICS:

I _{IH} Logic"1" Input Current	:	50uA max
I _{IL} Logic"0" Input Current	:	-2mA max
V _{OH} Logic"1" Output Voltage	:	2.7V min
V _{OL} Logic"0" Output Voltage	:	0.5V max
V _{IH} Logic"1" Input Voltage	:	2.0V min
V _{IL} Logic"0" Input Voltage	:	0.8V max
T _A Operating Temperature	:	0°C to 70°C
N _H Fanout"1" Output	:	20 TTL Load
N _L Fanout"0" Output	:	10 TTL Load
Supply Current	:	115mA TYP.

INPUT PULSE TEST CONDITION:

Pulse Voltage	:	3.2V
Pulse Width	:	50nS Min. or 100%Td
Duty Cycle	:	<50%
Pulse Rise Time	:	2nS(0.75 TO 2.4V)
Time Delay Measured	:	@1.5V level
Supply Voltage V _{cc}	:	5.0±0.25Vdc

YUAN DEAN SCIENTIFIC



Delay Line
12A/13A/22A SERIES

8 Pin

3 Independent Delays

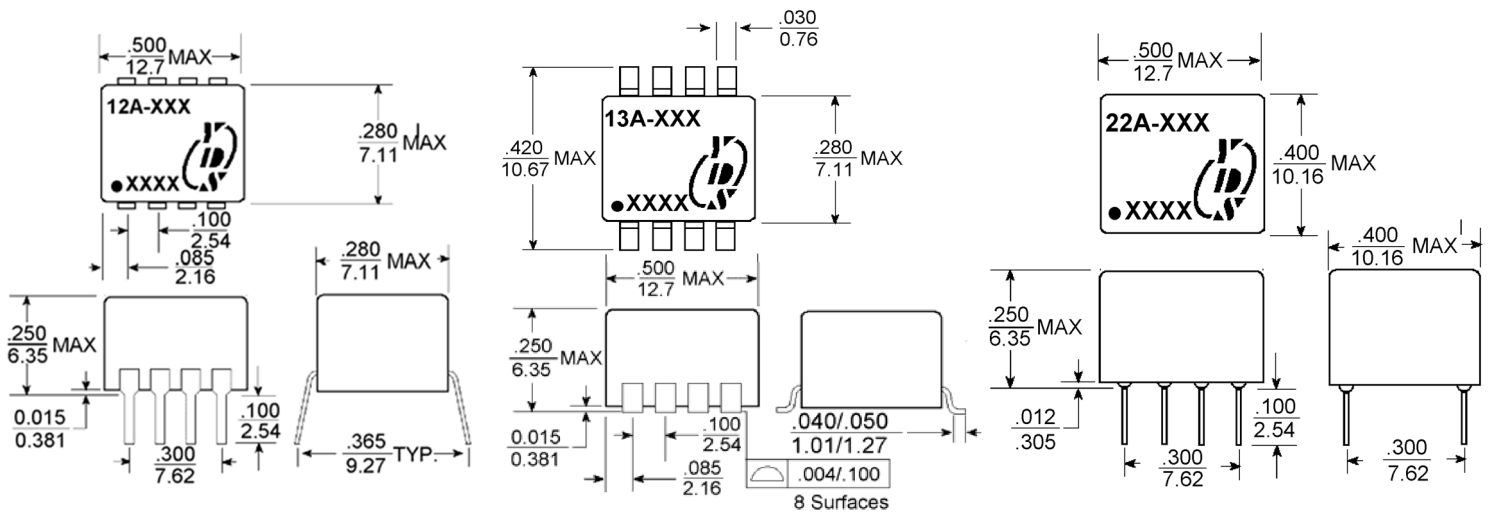
TTL Active



ELECTRICAL SPECIFICATIONS

PART NO.	DELAY TIME Td(ns)	RISE TIME Tr(ns)max
12A/13A/22A-005NL	5±2	4
12A/13A/22A-010NL	10±2	4
12A/13A/22A-015NL	15±2	4
12A/13A/22A-020NL	20±2	4
12A/13A/22A-025NL	25±5%	4
12A/13A/22A-050NL	50±5%	4
12A/13A/22A-075NL	75±5%	4
12A/13A/22A-100NL	100±5%	4
12A/13A/22A-250NL	250±5%	4
12A/13A/22A-500NL	500±5%	4

Markings and Dimensions



Dimensions: inches/mm Unless otherwise specified, all tolerances are ±.010/±0.25

Pin Connections

