

## FEATURES:

- 6-PIN PACKAGE
- TTL SCHOTTKY INTERFACED
- INHERENT DELAY:  $7 \pm 1$ ns
- TOTAL DELAY TOLERANCE:  $\pm 5\%$  OR  $\pm 1$ ns

## ELECTRICAL CHARACTERISTICS:

I <sub>IH</sub> Logic"1" Input Current	:	50uA max
I <sub>IL</sub> Logic"0" Input Current	:	-2mA max
V <sub>OH</sub> Logic"1" Output Voltage	:	2.4V min
V <sub>OL</sub> Logic"0" Output Voltage	:	0.4V max
V <sub>IH</sub> Logic"1" Input Voltage	:	2.0V min
V <sub>IL</sub> Logic"0" Input Voltage	:	0.8V max
T <sub>A</sub> Operating Temperature	:	0°C to 70°C
N <sub>H</sub> Fanout"1" Output	:	20 TTL Load
N <sub>L</sub> Fanout"0" Output	:	10 TTL Load
Supply Current	:	85mA Typ.

## INPUT PULSE TEST CONDITION:

Pulse Voltage	:	3V
Pulse Width	:	3T <sub>d</sub>
Duty Cycle	:	1000ns
Pulse Rise Time	:	2nS(0.75 to 2.4V)
Time Delay Measured	:	@1.5V level
Supply Voltage V <sub>cc</sub>	:	5.0 $\pm$ 0.25Vdc

YUAN DEAN SCIENTIFIC



Delay Line  
80A SERIES

16 Pin

TTL Schottky

3-Bit

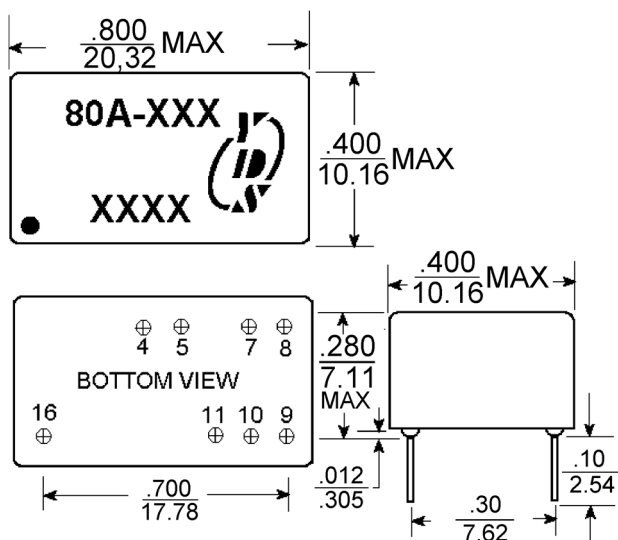
Programmable



ELECTRICAL SPECIFICATIONS

Program Pins Part numbers	0	1	2	3	4	5	6	7	Tol. Of Prog. Delay	Delay Time Per. Step	Delay Time Step Tol.	Maximum Delay Nominal Ref.IN
	3	0	0	0	1	1	1	1				
80A-014NL	7±1ns	1	2	3	4	5	6	7	±0.5ns	1ns	±0.3ns	14ns
80A-021NL	7±1ns	2	4	6	8	10	12	14	±0.6ns	2ns	±0.4ns	21ns
80A-028NL	7±1ns	3	6	9	12	15	18	21	±0.7ns	3ns	±0.5ns	28ns
80A-035NL	7±1ns	4	8	12	16	20	24	28	±0.9ns	4ns	±0.5ns	35ns
80A-042NL	7±1ns	5	10	15	20	25	30	35	±1ns	5ns	±0.5ns	42ns
80A-049NL	7±1ns	6	12	18	24	30	36	42	±1.2ns	6ns	±0.6ns	49ns
80A-056NL	7±1ns	7	14	21	28	35	42	49	±1.4ns	7ns	±0.7ns	56ns
80A-063NL	7±1ns	8	16	24	32	40	48	56	±1.6ns	8ns	±0.8ns	63ns
80A-070NL	7±1ns	9	18	27	36	45	54	63	±1.8ns	9ns	±0.9ns	70ns
80A-077NL	7±1ns	10	20	30	40	50	60	70	±2.0ns	10ns	±1ns	77ns
80A-091NL	7±1ns	12	24	36	48	60	72	84	±2.0ns	12ns	±1.2ns	91ns

Markings and Dimension



Dimensions: inches/mm

Unless otherwise specified, all tolerances are ±.010/±0.25

Pin Connections

