

FEATURES:

- 16-PIN PACKAGE
- ECL INPUT AND OUTPUT LEVEL
- INHERENT DELAY(T_o) : 3ns TYP
- FAN-OUT 70 ECL CAPACITY

YUAN DEAN SCIENTIFIC



ELECTRICAL CHARACTERISTICS:

I _{IH} Logic"1" Input Current	:	265uA max
I _{IL} Logic"0" Input Current	:	5uA max
V _{OH} Logic"1" Output Voltage	:	-0.96V min
V _{OL} Logic"0" Output Voltage	:	-1.65V max
V _{IH} Logic"1" Input Voltage	:	-0.98V min
V _{IL} Logic"0" Input Voltage	:	-1.63V max
TA Operating Temperature	:	0°C to 70°C
Supply Current	:	55mA Typ.

Delay Line
81A SERIES

16 Pin

ECL

3-Bit

Programmable

INPUT PULSE TEST CONDITION:

Pulse Voltage	:	-1V(-0.75V TO -1.75V)
Pulse Width	:	3T _d
Pulse Spacing	:	10T _d
Pulse Rise Time	:	2nS(0.75 to 2.4V)
Time Delay Measured	:	@-1.3V level
Supply Voltage V _{cc}	:	-0V
Supply Voltage V _{ee}	:	-5.2±5%V _{dc}

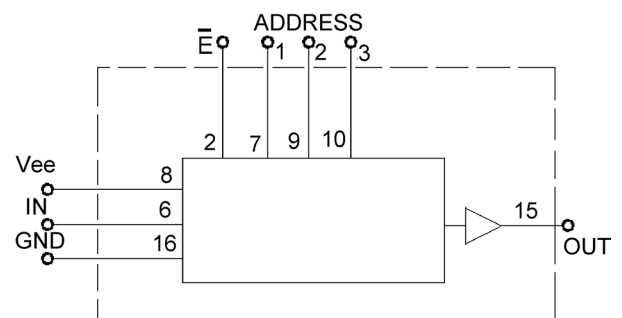
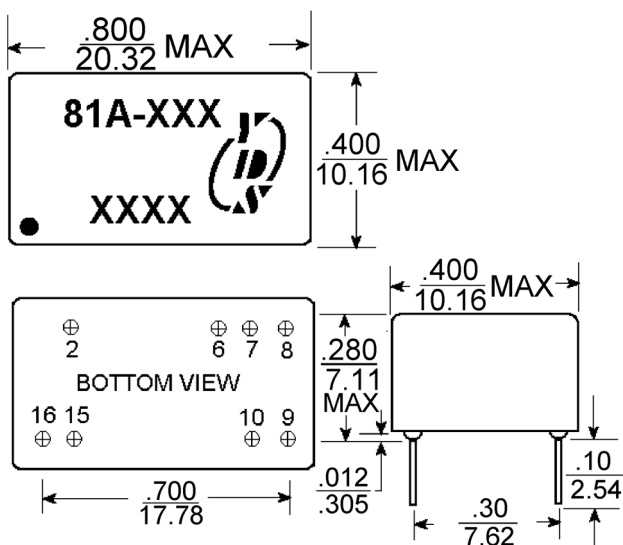


DELAY SPECIFICATIONS IN (ns)NANOSECONDS

Part numbers	Program Pins	0	1	2	3	4	5	6	7	Tol. Of Prog. Delay	Delay Time Per. Step	Delay Time Step Tol.	Maximum Delay Nominal Ref.IN
	3 2 1	0 0 0	1 0 1	2 0 1	3 0 1	4 1 0	5 1 0	6 1 1	7 1 1				
81A-010NL	3±1ns	1	2	3	4	5	6	7	±1ns	1ns	±0.1ns	10ns	
81A-017NL	3±1ns	2	4	6	8	10	12	14	±1ns	2ns	±0.2ns	17ns	
81A-024NL	3±1ns	3	6	9	12	15	18	21	±1ns	3ns	±0.3ns	24ns	
81A-031NL	3±1ns	4	8	12	16	20	24	28	±1.5ns	4ns	±0.4ns	31ns	
81A-038NL	3±1ns	5	10	15	20	25	30	35	±1.7ns	5ns	±0.5ns	38ns	
81A-045NL	3±1ns	6	12	18	24	30	36	42	±2ns	6ns	±0.6ns	45ns	
81A-052NL	3±1ns	7	14	21	28	35	42	49	±2.4ns	7ns	±0.7ns	52ns	
81A-059NL	3±1ns	8	16	24	32	40	48	56	±2.6ns	8ns	±0.8ns	59ns	
81A-066NL	3±1ns	9	18	27	36	45	54	63	±3.1ns	9ns	±0.9ns	66ns	
81A-073NL	3±1ns	10	20	30	40	50	60	70	±3.5ns	10ns	±1ns	73ns	
81A-087NL	3±1ns	12	24	36	48	60	72	84	±4.2ns	12ns	±1.2ns	87ns	

Markings and Dimensions

Pin Connections



Dimensions: inches/mm

Unless otherwise specified, all tolerances are ±.010/±0.25